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142 #RSTIN	#RESIN	1	2
3 #RSTOUT	#RESOUT	3	4
23 SDA1	I2C GEN0 SDA	5	6
24 SCL1	I2C GEN0 SCL	7	8
Digital GND	SGND	9	10
116 AD15	A15_AD15	11	12
115 AD14	A14_AD14	13	14
114 AD13	A13_AD13	15	16
113 AD12	A12_AD12	17	18
112 AD11	A11_AD11	19	20
111 AD10	A10_AD10	21	22
106 AD9	A9_AD9	23	24
105 AD8	A8_AD8	25	26
102 AD7	A7_AD7	27	28
101 AD6	A6_AD6	29	30
100 AD5	A5_AD5	31	32
99 AD4	A4_AD4	33	34
98 AD3	A3_AD3	35	36
97 AD2	A2_AD2	37	38
96 AD1	A1_AD1	39	40
95 AD0	A0_AD0	41	42
93 #ALE	ALE	43	44
90 #RD	#RD	45	46
91 #WRL	#WR	47	48
75 #WRH	#WRH	49	50
A16	#PSEN	51	52
7 #CS0 (SRAM)	#CS0	53	54
8 #CS1 (CS8900)	#CS1	55	56
9 #CS2	#CS2	57	58
10 #CS3	#CS3	59	60
84 CAN1 RX	CAN0 RX	61	62
87 CAN1 TX	CAN0 TX	63	64
USB DEBUG D+	USB DEBUG D+	65	66
USB DEBUG D-	USB DEBUG D-	67	68
76 SCLK0	CNTRL SPI CLK	69	70
67 MRST0	CNTRL SPI MRST	71	72
68 MTSR0	CNTRL SPI MTSR	73	74
82 P4.2	CNTRL SPI #CS_NSS	75	76
25 SDA2	CNTRL I2C SDA	77	78
26 SCL2	CNTRL I2C SCL	79	80
NC	USB HOST D+	81	82
NC	USB HOST D-	83	84
NC	USB DEV D+	85	86
NC	USB DEV D-	87	88
CANH control physical layer (CAN1)	CANHO	89	90
CANL control physical layer (CAN1)	CANLO	91	92
CPU's Vcc 3V3 or 5V	VCC_CM	93	94
Vcc 3V3 from reg	+3V3	95	96
Vcc 5V from reg	+5V	97	98
Digital GND	SGND	99	100

Tyco Amp 100 Way

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